

Electronic Acknowledgement Receipt

EFS ID:	9060009
Application Number:	10044217
International Application Number:	
Confirmation Number:	9153
Title of Invention:	System and method for connecting a logic circuit simulation to a network
First Named Inventor/Applicant Name:	Robert M. Zeidman
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Filer Authorized By:	Alexander A. Courtade
Attorney Docket Number:	6257-16302
Receipt Date:	17-DEC-2010
Filing Date:	19-NOV-2001
Time Stamp:	11:45:24
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$180
RAM confirmation Number	11065
Deposit Account	501505
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed (SB/08)	updated_IDS.pdf	612171 <small>file92edcd25aa51b7b15d445a1265c1c7b518a2</small>	no	5
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	30302 <small>ed9513856188acdf97885c1141aa3b7e1e751e10366</small>	no	2
Warnings:					
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Total Files Size (in bytes):			642473		
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